

## Freeform Search

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|------------------|--|
| <b>Database:</b> | US Pre-Grant Publication Full-Text Database<br>US Patents Full-Text Database<br>US OCR Full-Text Database<br>EPO Abstracts Database<br>JPO Abstracts Database<br>Derwent World Patents Index<br>IBM Technical Disclosure Bulletins |
| <b>Term:</b>     | (power adj controller) and (digital adj implementation) and algorithms   |
| <b>Display:</b>  | <input type="text" value="10"/> Documents in <b>Display Format:</b> <input type="text" value="-"/> Starting with Number <input type="text" value="1"/>   |
| <b>Generate:</b> | <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image   |

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### Search History

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**DATE:** Monday, March 22, 2004    [Printable Copy](#)    [Create Case](#)

| <u>Set</u><br><u>Name</u> | <u>Query</u>  | <u>Hit</u><br><u>Count</u> | <u>Set</u><br><u>Name</u><br>result set |
|---------------------------|---|----------------------------|---|
| side by side              |   |                            |   |
|                           | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>                       |                            |   |
| <u>L22</u>                | (power adj controller) and (digital adj implementation) and algorithms              | 24                         | <u>L22</u>                              |
| <u>L21</u>                | L20 and algorithms  | 10                         | <u>L21</u>                              |
| <u>L20</u>                | L19 and stor\$3   | 10                         | <u>L20</u>                              |
| <u>L19</u>                | L18 and (look\$up adj table)  | 10                         | <u>L19</u>                              |
| <u>L18</u>                | L1 and 315/\$.ccls.   | 13                         | <u>L18</u>                              |
| <u>L17</u>                | L1 and 3315/\$.ccls.  | 0                          | <u>L17</u>                              |
| <u>L16</u>                | L15 and lamp  | 13                         | <u>L16</u>                              |
| <u>L15</u>                | L14 and program\$4 and (integrated adj circuit)                                     | 62                         | <u>L15</u>                              |
| <u>L14</u>                | L13 and stor\$3   | 69                         | <u>L14</u>                              |
| <u>L13</u>                | (master adj controller) and "ASIC" and look\$up and data and digital and parameters | 70                         | <u>L13</u>                              |
| <u>L12</u>                | l1 and "ASIC"   | 13                         | <u>L12</u>                              |
| <u>L11</u>                | 4797599.pn.   | 2                          | <u>L11</u>                              |
| <u>L10</u>                | L9 and IC   | 5                          | <u>L10</u>                              |

(master adj controller) and processor and (logic adj unit) and digital and

|           |   |    |           |
|-----------|---|----|-----------|
| <u>L9</u> | data and stor\$3 and look\$up   | 84 | <u>L9</u> |
| <u>L8</u> | l1 and EEPROM   | 0  | <u>L8</u> |
| <u>L7</u> | l1 and EPROM  | 0  | <u>L7</u> |
| <u>L6</u> | L1 and (digital adj data adj stor\$3)                                 | 8  | <u>L6</u> |
| <u>L5</u> | L4 and digital  | 13 | <u>L5</u> |
| <u>L4</u> | L3 and algorithms   | 13 | <u>L4</u> |
| <u>L3</u> | l1 and ROM  | 13 | <u>L3</u> |
| <u>L2</u> | L1 and IC   | 14 | <u>L2</u> |
| <u>L1</u> | (gas adj discharge) and lamp and (master adj controller) and feedback | 27 | <u>L1</u> |

END OF SEARCH HISTORY